IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors

Nhon QUACH, et al.

Serial No.

To Be Assigned (Continuation of 09/608,959)

Filed

Concurrently Herewith

For

APPARATUS AND METHOD FOR PROTECTING

CRITICAL RESOURCES AGAINST SOFT ERRORS IN

HIGH PERFORMANCE MICROPROCESSORS

Group Art Unit

2184 (Anticipated)

Examiner

IQBAL, NADEEM (Anticipated)

COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, VA 22313-1450

:

PRELIMINARY AMENDMENT AND RESPONSE

SIR:

Prior to examination of the above-referenced Continuation, Applicants respectfully request the following amendments and remarks be admitted.